

AUSTIN MALISZEWSKI

WWW.AUSTINMALISZEWSKI.COM | AUSMAL@UMICH.EDU | +1.773.718.0962 (USA) | +44.7510.458234 (UK)

EDUCATION

UNIVERSITY OF MICHIGAN

Ann Arbor, MI, USA

BSEng Computer Engineering

Cumulative GPA: 3.659/4.000

Expected May 2015

UNIVERSITY OF MANCHESTER

Study Abroad | Manchester, England

Jan. - June 2014

- Studied CS, Math, and Art History.

UNIVERSITÉ DE TECHNOLOGIE DE TROYES

Summer Study Abroad | Troyes, France

May - July 2012

- Courses in French Culture & Managing Engineering Projects.
- Increased proficiency in French.

NOTABLE COURSEWORK

EECS 482*: Operating Systems

EECS 470: Computer Architecture

EECS 373*: Microprocessor Syst.

EECS 370: Intro. to Comp. Org. (IA)

EECS 320: Semiconductor Devices

EECS 312*: Dig. Integrated Circuits

EECS 281: Data Structures & Algos.

COMP35112: Chip Multiprocessors

COMP34512: Knowledge Representation and Reasoning

(* denotes currently ongoing coursework)

SKILLS

LANGUAGES: C, C++, C#, SQL, Java, PHP/HTML, Python, ARM Thumb, MATLAB, Mathematica, bash

HDLs: SystemVerilog, Verilog, (minimal) VHDL

EDA TOOLS: Xilinx ISE, Mentor Graphics ModelSim, Synopsys VCS, Cadence Virtuoso

(listed in approx. descending familiarity)

PERMANENT ADDRESS

6860 N. Moselle Avenue

Chicago, IL 60646

United States

WORK EXPERIENCE

PIXEL VELOCITY, INC. | *Engineering Intern*

June - Nov. 2014 | Ann Arbor, MI

- Created hardware accelerator for connected-component labeling.
- Integrated open source MJPEG encoding IP into PV-3000 design using custom wrapper hardware and Xilinx DMAs.
- Developed custom Yocto Linux environment for Pixel PV-3000 camera including custom drivers for sensor programming, image acquisition and hardware JPEG encoding.

UNIVERSITY OF MICHIGAN | *Assistant in Research*

Jan. 2013 - present | Ann Arbor, MI

- Helped develop custom preemptive operating system in ANSI C and ARM Thumb for the Talking Book, a low power, ARM Cortex-M0 based, audio-driven computer intended for illiterate people in the poorest parts of the world.
- Wrote and tested device drivers in ANSI C for many of the Talking Book's peripherals, including sound, clock, and capacitive buttons.

UNIVERSITY OF MICHIGAN | *Instructional Aide*

Aug. - Dec. 2013 and Jan. 2015 - present | Ann Arbor, MI

- Led a discussion section of 32 students for junior level course "Intro. to Computer Organization" during Fall 2013 semester.
- Maintained autograder, scored student projects and investigated possible project plagiarism.
- Currently leading discussion section for "Intro to Operating Systems"

SYSTEM DEVELOPMENT INTEGRATION, LLC (SDI) | *Intern*

June - Aug 2011 | Chicago, IL

- Developed custom interfaces to a network monitoring system for a large governmental agency.
- Provided desktop and server support services to a large municipal corporation. Responsibilities included writing an application to monitor and document contract compliance, as well as troubleshooting and resolving Active Directory replication issues.

PROJECTS

MARINA: A PROCESSOR

EECS 470: Computer Architecture | Fall 2013

- Worked in team of 5 to design superscalar, 2-way simultaneously multi-threaded, out-of-order processor in synthesizable SystemVerilog to implement a subset of the Alpha ISA.

LC2K-MC

- Designed multi-core implementation of an LC2K ISA machine using MESI snooping caches in SystemVerilog.

PAYTON CITYWIDE MATH CIRCLE WEBSITE

- Designed and implemented custom web application in C# for management of not-for-profit PCMC's biweekly math enrichment sessions for middle and high school students in the Chicagoland area.